

10/023,075  
**EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1440	('VRML' or 'virtural reality modeling language') and (platfor\$2) and ('image file') and (('circuit' near2 'design') or 'PCB') and ('www' or 'world wide web') and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:01
L4	924	('VRML' or 'virtural reality modeling language') and (platfor\$2) and ('image file') and ('circuit' near2 'design') and ('www' or 'world wide web') and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:02
L5	184	('VRML' or 'virtural reality modeling language') and (platfor\$2) and ('image file') and ('circuit' near2 'design') and ('www' or 'world wide web') and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:04
L6	46	('VRML' or 'virtural reality modeling language') and (platfor\$2) and ('image file') and ('circuit' near2 'design') and ('www') and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:04
L7	10	('VRML' or 'virtural reality modeling language') and (platfor\$2) and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:06

## EAST Search History

L8	10	((('PCB' or 'circuit design')." "ab.") and ('VRML' or 'virtual reality modeling language') and (platfor\$2) and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:07
L9	10	((('PCB' or 'circuit design')." "ab.") and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:08
L10	10	('PCB'.ab. or 'circuit design'.ab.) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:09
L11	10	(PCB.AB. or 'circuit design'.AB.) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:16
L12	25698362	(PCB.AB. or 'circuit design'.AB.) (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:11

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L13	3546682	(PCB.AB. or 'circuit design'.AB.) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:12
L14	4059286	'circuit design'.AB. and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:12
L15	3537283	('circuit design'.AB.) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:15
L16	10	((PCB.AB.) or ('circuit design'.AB.)) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:23
L17	0	((PCB.AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:15
L18	6106	('circuit design'.AB.) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 16:46

## EAST Search History

L19	0	("PCB".AB. or "circuit design".AB.) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:16
L20	0	("circuit design".AB.) and 'vrml' and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:17
L21	0	("circuit design".AB.) and 'VRML' and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:17
L22	31333	((("circuit design".AB.) or ("PCB".AB.)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:19
L23	1	((("circuit design".AB.) or ("PCB".AB.)) and 'VRML' and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:19
L24	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('image file') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:24

## EAST Search History

L25	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:24
L26	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (external near2 data\$4) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:24
L27	2	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (location same (reference or coordinat\$3)) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:28
L28	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and ('x' near2 'y' same 'rotation') and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:28
L29	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (('X' near2 'Y') same 'rotation') and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:29

## EAST Search History

L30	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (('X' near2 'Y') same coordinate\$2) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:29
L31	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (rotat\$4 same coordinate\$2) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:29
L32	0	((("PCB".AB.) or ("circuit design".AB.)) and ('VRML' or 'virtual reality modeling language') and ('circuit' near2 'design') and (('www' or 'web') same browser) and (rotat\$4 same coordinat\$2) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 14:29
L33	0	("circuit design".AB.) and "'www'." and 'VRML' and 716/1-15.ccls. and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 16:47
L34	0	("circuit design".AB.) and www and 'VRML' and 716/1-15.ccls. and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 16:49
L35	0	gusikhin-oleg.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 16:49
L36	0	gusikhin-oleg-yurivich.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 16:50

## EAST Search History

S4	23	(VRML) and (platfor\$2) and ('image file') and (JPEG) and (@ad<"20011213" or @rlad<"20011213" or @prad<"20011213")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/01 13:59
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